ModelSource 3000 Hardware Modeling Systems

- Provides High-Performance, Full Functional Simulation Models of i960® Processor Devices
- Low Cost, Single Model Configurations Now Available
- Correctly Represents Documented and Undocumented Device Behavior
- Enables the Execution of Actual System Code During Design Verification
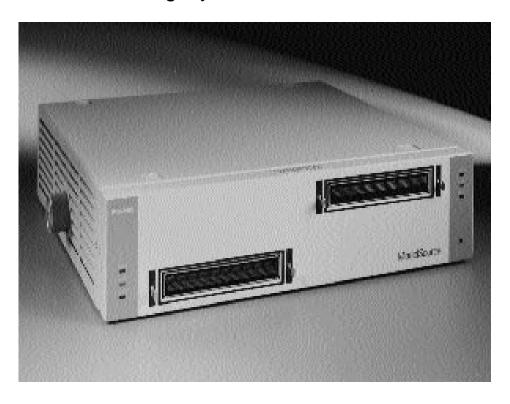- Industry Standard – Supports Over 40 Simulators

The ModelSource 3000* Series of hardware modeling systems provides fully-functional simulation models for all i960 processors.

The ModelSource 3000 Series is the newest line of hardware modeling products from Synopsys (formerly known as Logic Modeling). A compact, desktop package and modular architecture significantly lower the entry price of hardware modeling. A system configured to model a single i960 processor will cost less than a third of the price of an existing LM-family* system.

A hardware model uses the actual device as part of the simulation model. This ensures accuracy and provides access to all facets of a device's function, including any undocumented behavior. Hardware models also permit the execution of actual system code during simulation for complete design verification.

Designers use hardware models in a wide variety of applications including board simulation and ASIC pins-out verification, i.e. verifying the interaction of an ASIC design with models of the external VLSI components.

The ModelSource 3000 Series features the same level of integration as the industry-standard, high-capacity LM-family of hardware model servers. ModelSource products are seamlessly integrated into all major EDA simulator environments and completely compatible with other modeling solutions.

Models are available for all i960 processors, including the Hx and Jx product lines.

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